

We Claim:

1. A semiconductor component, comprising:

a semiconductor chip with a semiconductor memory having an array with a plurality of memory cells;

at least one connection contact;

an electrically conductive connection between said at least one connection contact and said semiconductor memory;

at least one converter device selected from the group consisting of a digital-analog converter and an analog-digital converter incorporated in said electrically conductive connection for enabling a utilization of said semiconductor memory with analog signals; and

wherein a number of said connection contacts for said semiconductor chip is less than a number of connection contacts required for a utilization of said semiconductor memory with digital signals.

2. The component according to claim 1, wherein said at least one converter device includes digital-analog converters and analog-digital converters disposed to enable read and write operations by outputting and inputting analog data.

3. The component according to claim 1, wherein said at least one converter device includes at least one analog-digital converter disposed to enable control of read and write operations and/or to address memory locations by inputting analog control signals.

4. The component according to claim 1, wherein said at least one converter device includes digital-analog converters and analog-digital converters disposed to enable both read and write operations and control thereof by outputting and inputting exclusively analog data and by inputting exclusively analog control signals.